

FIG. 1A

131
✓

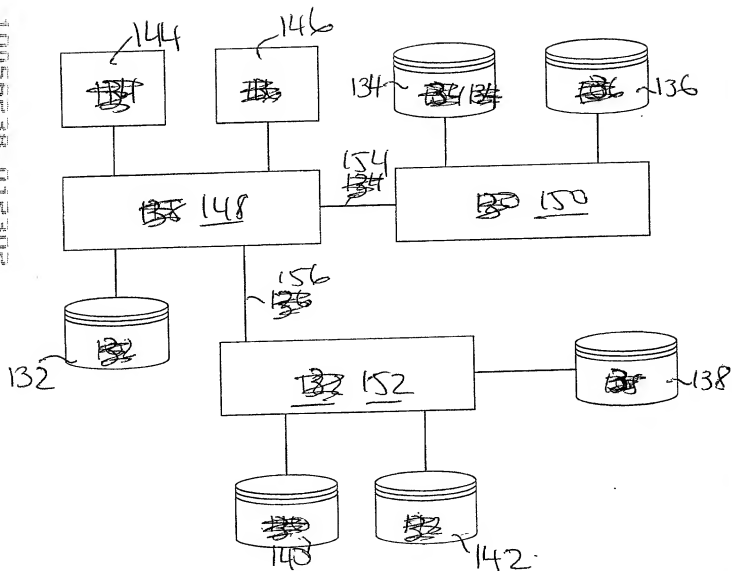
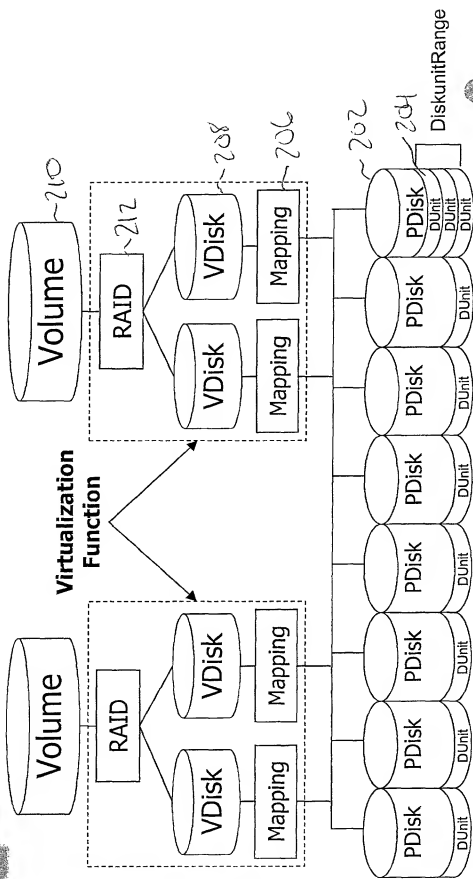


FIG. 1B

Andiamo Virtualization Model



andiamo
V I S I O N E S

FIG. 2

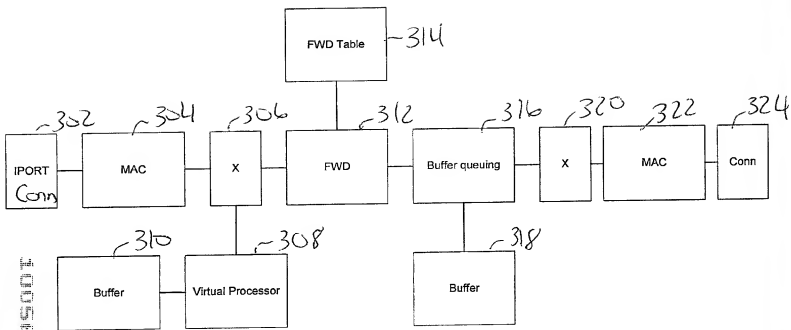


FIG. 3A

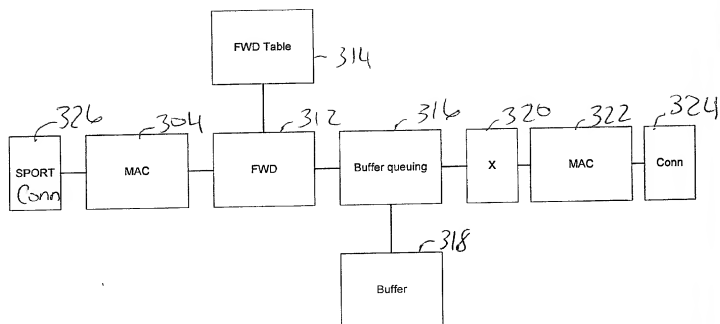


FIG. 3B

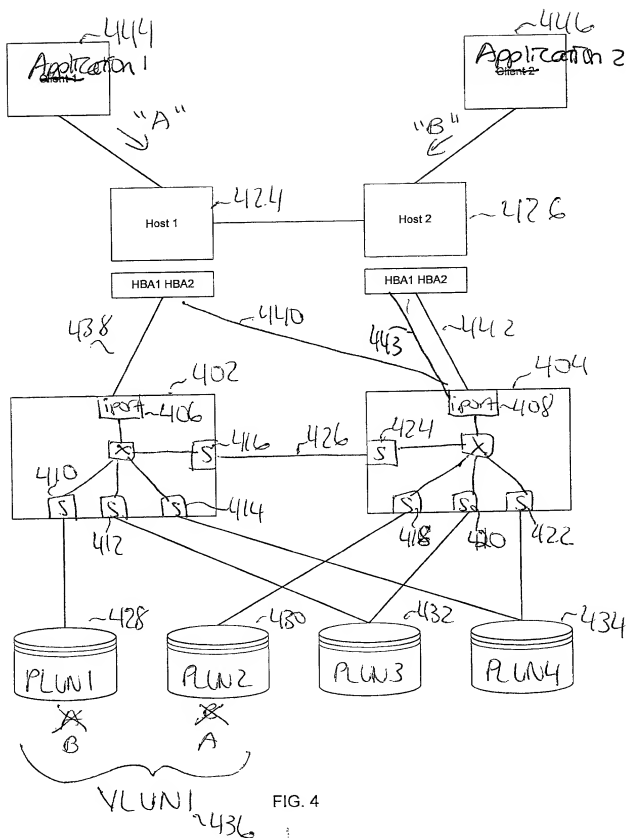


FIG. 4

10056236 012302

~506
Write (block B, address, length=4KB)

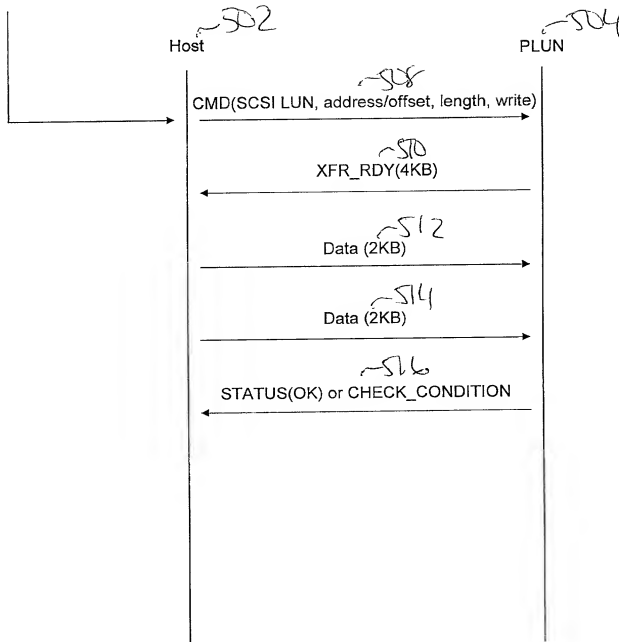


FIG. 5

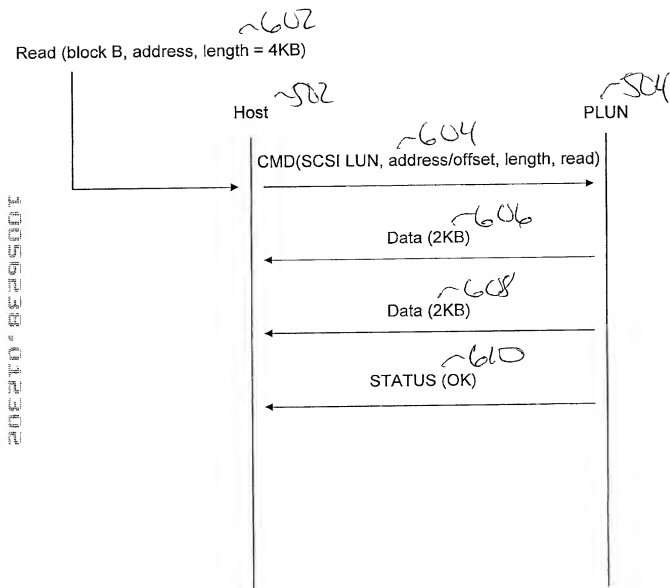


FIG. 6

Host ~702 vport ~704 PLUN1 ~706 PLUN2 ~708

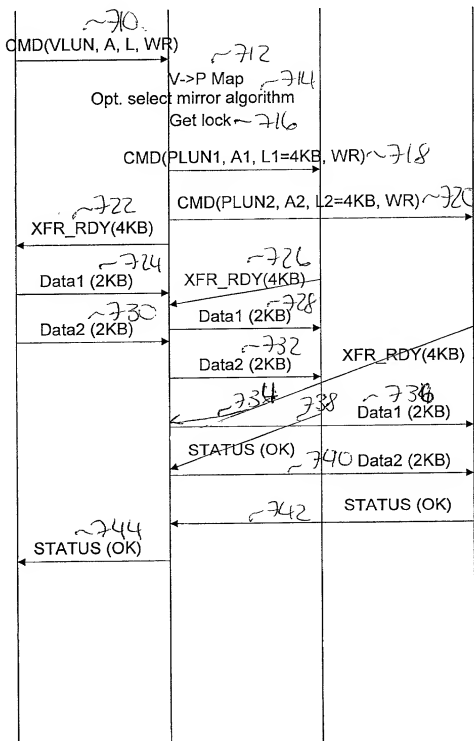


FIG. 7

~702
Host

~704
vport

~706
pLUN1

~708
pLUN2

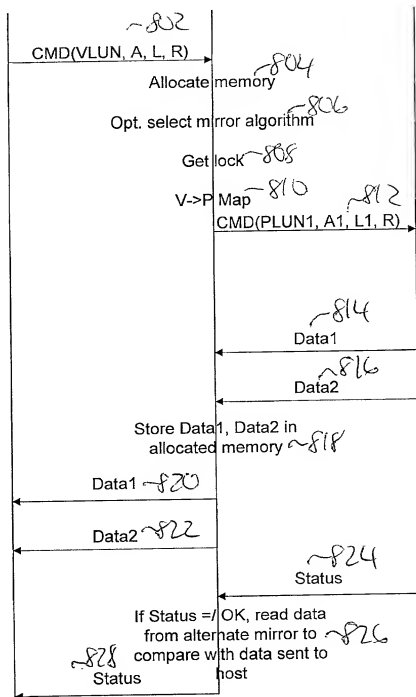
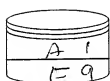
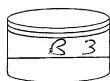


FIG. 8



PLUN1

902



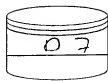
PLUN2

904



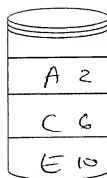
PLUN3

906



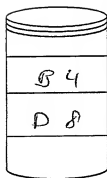
PLUN4

908



PLUN5

910

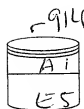


PLUN6

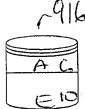
912

RAID $\phi + \phi$

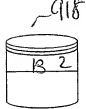
FIG. 9A



PLUN1



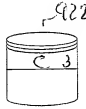
PLUN2



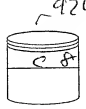
PLUN3



PLUN4



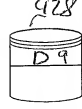
PLUN5



PLUN6



PLUN7



PLUN8

RAID $\phi + \phi$

FIG. 9B

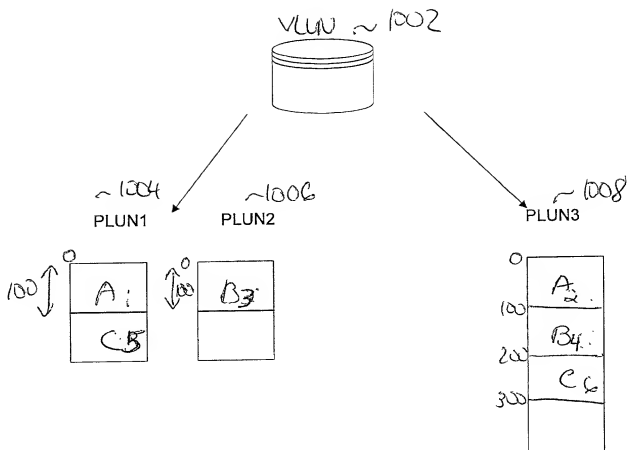


FIG. 10A

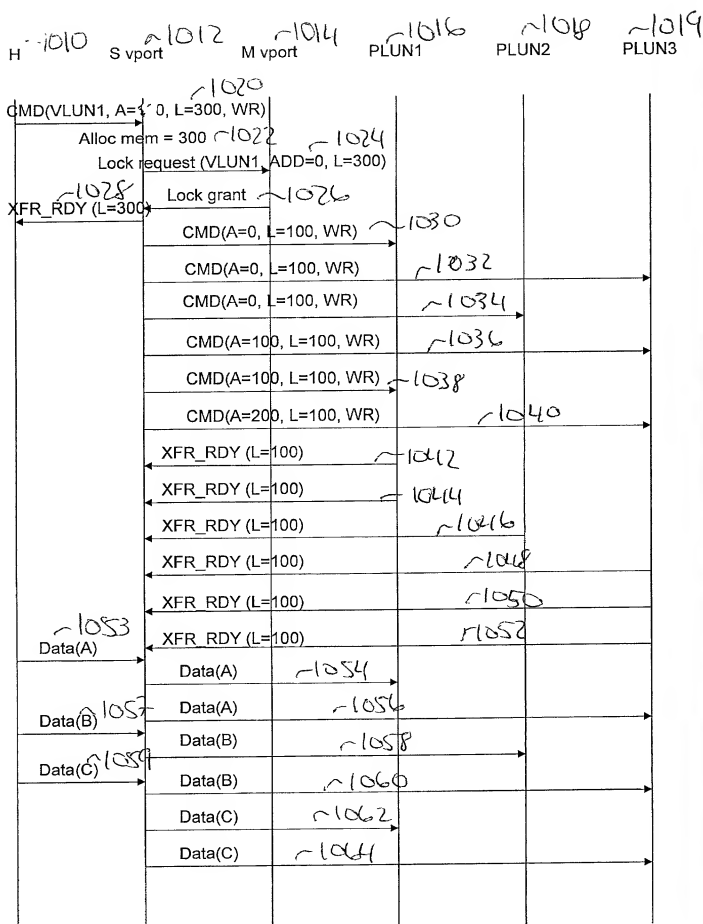


FIG. 10B

H ~1010 S vport ~1012 M vport ~1014 PLUN1 ~1016 PLUN2 ~1018 PLUN3 ~1019

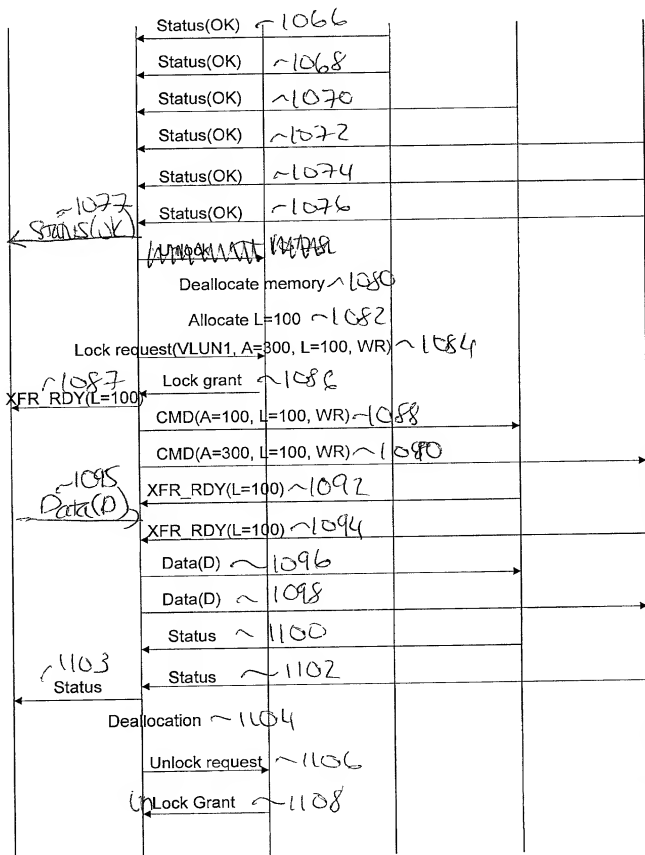


FIG. 10C